PATENT NUMBER and ISSUE DATE

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U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	CAU	EXAMINER
10061504	02/01/2002	327	295	2816	Linh Nguyen

**APPLICANTS: Neff Robert

**CONTINUING DATA VERIFIED: LN

** FOREIGN APPLICATIONS VERIFIED: LN

PUBLICATION DO NOT PUBLISH ☐ RESCIND ☐

Foreign priority claimed ☐ yes ☑ no

35 USC 119 conditions met ☐ yes ☑ no

Verified and Acknowledged Examiner's initials LNN

ATTORNEY DOCKET NO 10010205-1

TITLE : Interleaved clock signal generator having serial delay and ring counter architecture

U.S.DEPT. OF COMM./PAT.& TM-PTO-436LINE / (12-93)

NOTICE OF ALLOWANCE MAILED	Assistant Examiner	CLAIMS ALLOWED: Total Claims / Print Claim for O.G
ISSUE FEE: Amount Due / Date Paid	Primary Examiner	DRAWING: Sheets Drwg. / Figs.Drwg. / Print Fig.
☐ TERMINAL DISCLAMER	PREPARED FOR ISSUE	Application Examiner

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